

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	METHOD AND STRUCTURE FOR FORMING STRAINED Si FOR CMOS DEVICES	
Application Number :		
Date :		
First Named Applicant:	An L. Steegen	
Confirmation Number:		
Attorney Docket Number:	FIS920030236US1	
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Submitted By:	Elec. Sign.	Sign. Capacity
Joseph P. Abate Registered Number: 30238	Joseph P. Abate /s/	Attorney

Documents being submitted:	Files
us-assignment	FIS920030236US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030236Assignment1.tif FIS920030236Assignment2.tif
us-request	FIS920030236US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FIS920030236US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	fis920030236-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd FIS920030236DRAWING13.tif FIS920030236DRAWING2.tif FIS920030236DRAWING3.tif FIS920030236DRAWING4.tif FIS920030236DRAWING5.tif FIS920030236DRAWING6.tif FIS920030236DRAWING7.tif
us-declaration	FIS920030236Declaration1.tif
us-declaration	FIS920030236Declaration2.tif
<b>Comments</b>	

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